

**IN THE ABSTRACT OF THE DISCLOSURE:**

Please amend the Abstract of the Disclosure to read as follows:

A circuit arrangement for signal processing in a receiver and/or transmitter of a radio system, wherein the circuit arrangement nodes (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) are arranged to execute at least one operation[, the]]. The circuit arrangement comprising comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322, 326) for dividing at least one of the signals or signal components according to the one or more predetermined division criteria for signal classes[[,]] and the circuit arrangement comprising comprises means (304, 306, 308, 310, 312, 314, 316, 318, 320, 322) for executing predetermined operations signal-classwise.